

# Menta's Embedded FPGA Successfully Evaluated by Partners for the EDA-SoC program

*Embedded FPGAs enable programmability in SoC design platform for use in defense systems*

MONTPELLIER, France, November 16, 2015 — Menta SAS, a provider of embedded FPGA (eFPGA) Intellectual Property (IP), today announced that the partners for the EDA-SoC program of the European Defense Agency have successfully evaluated Menta's embedded FPGA fabric.

The EDA-SoC program is supported by France, Italy, Germany and Norway through the European Defense Agency. The program gathered 14 partners from major defense equipment manufacturers, technology providers, and design centers to define an optimal SoC development platform, including the complete flow from design to manufacture and test. The SoC platform embeds a 32-bit RISC processor, the Menta embedded FPGA fabric and other standard IP blocks.

As a result of the EDA-SoC program, suppliers now have access to a fully tested and programmable SoC platform that is ideal for use in a wide range of telecom, cybersecurity and defense applications. Menta was selected for the EDA-SoC project based on the flexibility of its embedded FPGA IP, as well as the robust capabilities of the company's ORIGAMI software design environment.

For the EDA-SoC project, Menta supplied a fully functional embedded FPGA fabric with embedded logic blocks (eLBs), global clocks, memory, ALUs, and an SPI interface. The floorplan techniques were optimized, and custom cells were designed for the 65nm process to improve area density.

"Menta is thrilled to have been a part of the EDA-SoC program," said Laurent Rougé, Founder and CEO of Menta. "Our participation really highlighted the versatility of our embedded FPGAs, as well as the full capabilities of the ORIGAMI design environment. As a result of these efforts, Menta is seeing great traction among other EDA-SoC partners seeking to use embedded FPGAs for their applications."

Menta provides both custom and pre-defined embedded FPGAs for integration in a wide range of SoCs. The embedded FPGAs are supplied with a proven EDA tool that supports design from HDL design to bitstream with synthesis, mapping, place and route. Menta's embedded FPGAs and associated software are available now. For more information, please visit [www.menta-efpga.com/technology](http://www.menta-efpga.com/technology), or contact our customer support team at [contact@menta-efpga.com](mailto:contact@menta-efpga.com).

## **About Menta**

Menta is a privately held company based in Montpellier, France. The company provides embedded FPGA (eFPGA) technology for System on Chip (SoC), ASIC or System in Package (SiP) designs, from EDA tools to IP generation. Menta's programmable logic architecture is based on scalable, customizable and easily programmable architecture created to provide programmability for next-generation ASIC design with the benefits of FPGA design flexibility. For more information, visit the company website at: [www.menta-efpga.com](http://www.menta-efpga.com)