

FOR IMMEDIATE RELEASE

Menta Embeds sureCore Low Power SRAM IP on TSMC's 28nm Process

eFPGAs enable programmability in SoCs for low power products

Montpellier, France, August 29, 2017 — [Menta SAS](#), a leading embedded FPGA (eFPGA) Intellectual Property (IP) provider, and [sureCore Ltd.](#), a low-power SRAM IP leader, today opened the doors to creative power-sensitive design in leading-edge applications with the availability of eFPGA IP with embedded sureCore PowerMiser™ low-power SRAM IP. The IP is optimized for the TSMC 28 nanometer process.

Menta's eFPGAs enable programmability to be embedded into complex SoCs, allowing changes to be made to the RTL at will, post-production, thereby eliminating costly re-spins. This capability is critical to meeting the sometimes conflicting requirements of changing standards, security updates and shrinking time-to-market windows of mobile and consumer products, IoT devices, networking and automotive ICs.

SureCore's single port synchronous low power PowerMiser IP delivers more than 50% dynamic power savings and up to 38% static power savings compared to today's commercially available alternatives. PowerMiser is ideal for leading-edge devices demanding long battery life and minimal operating and stand-by power consumption. The macro is process agnostic and silicon proven in both CMOS and FDSOI manufacturing processes.

“Menta is pleased to demonstrate the flexibility of its eFPGA IP by integrating sureCore innovative low-power SRAM,” said Yoan Dupret, Managing Director of Menta. “Adding sureCore SRAM IP within our eFPGA IP and Origami Programmer software only required a couple of weeks thanks to sureCore support and SRAM IP ease of integration.”

"This collaboration represents the first stage in an on-going relationship where sureCore provide

custom memory solutions on Menta's customers' requests. Our commitment is to help Menta provide forward-looking designs with the lowest power eFPGA," explained Eric Gunn, sureCore's Chief Operating Officer.

The eFPGA core IP includes 800 LUT6 equivalent, six 18bit MAC and 6Kb of sureCore single port SRAM in the TSMC 28HPC+ technology node running at 0.9V. The new IP targets mobile, base stations and IoT products for applications such as flexible interface bridging, power-up management or sensor hubs. Menta eFPGA IP including sureCore SRAM IP can be customized with various amount of LUTs, DSPs and SRAM based on customer requirements; and in the most widely used 40nm ULP and 28nm LP processes, as well as 28nm FDSOI technology.

Availability

Menta's eFPGA IP core datasheet and associated software are available now. For more information, please visit www.menta-efpga.com, or contact our customer support team at info@menta-efpga.com.

About Menta

Menta is a privately held company based in Montpellier, France. The company provides embedded FPGA (eFPGA) technology for SoC, ASIC or SASSP designs. Menta's programmable logic architecture is based on scalable, customizable and easily programmable architecture created to provide programmability for next-generation ASIC design with the benefits of eFPGA design flexibility. For more information, visit the company website at: www.menta-efpga.com

About sureCore

sureCore Limited is an SRAM IP company based in Sheffield, UK, developing low power memories for current and next generation, silicon process technologies. Its award-winning, world-leading, low power SRAM design is process independent and variability tolerant, making it suitable for a wide range of technology nodes. This IP will help SoC developers meet both challenging power budgets and manufacturability constraints posed by leading edge process nodes.

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