

Press inquiries:

Tiana Dixon
(503) 708-1925
tiana.dixon@gmail.com



FOR IMMEDIATE RELEASE

Menta Selected as Sole Provider of Embedded FPGAs for European Processor Initiative

eFPGAs improve density, programmability of SoCs for high-performance computing and automotive applications

SOPHIA-ANTIPOLIS, France, November TBD, 2018 — [Menta SAS](#), a provider of embedded FPGA (eFPGA) Intellectual Property (IP), today announced that it has been selected as the sole provider of embedded FPGA intellectual property for the European Processor Initiative (EPI).

The European Processor Initiative brings together 23 partners from 10 European countries with the objective of co-designing, manufacturing and bringing to market a system that supports the high-performance computing requirements of exascale machines. Members of the EPI consortium include Atos, BMW, CEA, Infineon and ST Microelectronics.

“Menta is an ideal partner for the European Processor Initiative,” said Philippe Notton, general manager of EPI. “Their high-performance eFPGA technology aligns perfectly with the objectives of our program. Menta’s pure digital IP guarantees a very fast delivery. The technology is provided with an ASIC-like design for test and verification methodology that is critical to enabling first time success of IPs in the most advanced process nodes.”

For the EPI, Menta supplies several embedded FPGA fabrics with a large number of embedded logic blocks (eLBs), as well as DSP and memory. The IP is supported by Menta’s unique eFPGA IP specification software, Origami Designer, which enables silicon architects to fine-tune the eFPGA fabrics to the specific requirements of high-performance computing and automotive control unit applications. The IP is provided on an advanced 7nm process and verified in standard ASIC flow. SoC’s incorporating Menta’s eFPGA technology will begin to tape out in 2020.

“Menta is thrilled to be selected by the EPI,” said Vincent Markus, CEO of Menta. “So far, Menta has produced its eFPGA IPs on processes from 130nm to 14nm. Our participation in this consortium really highlights the ability of our eFPGAs to be provided on any process technology,

even the most advanced, such as 7nm, thanks to our third-party standard cells approach. As a result of these efforts, we expect to see Menta eFPGAs driving programmability in a significant share of new high performance computing and automotive applications.”

Menta provides embedded FPGAs for integration in a wide range of SoCs. The embedded FPGAs are supplied with a proven EDA tool, Origami Programmer, that supports design from HDL design to bitstream with synthesis, mapping, place and route. Menta’s embedded FPGAs and associated software are available now. For more information, please visit www.menta-efpga.com, or contact our customer support team at info@menta-efpga.com.

-ends-

About Menta

Menta is a privately held company based in Sophia-Antipolis, France. The company provides embedded FPGA (eFPGA) technology for System on Chip (SoC), ASIC or System in Package (SiP) designs, from EDA tools to IP generation. Menta's programmable logic architecture is based on scalable, customizable and easily programmable architecture created to provide programmability for next-generation ASIC design with the benefits of FPGA design flexibility. For more information, visit the company website at: www.menta-efpga.com

Origami Designer, Origami Programmer and eFPGA Core IP are registered trademarks of Menta SAS. All other trademarks and tradenames are the property of their respective holders.