

Press Invitation

Embedded FPGA reaches a new stage of industrial maturity – Menta at Embedded World 2026

Embedded World 2026

March 10–12, 2026 | Nuremberg, Germany

Hall 4 – IC & IP Design Area | Booth 4-459

As the embedded industry converges in Nuremberg for Embedded World 2026, a structural shift is gaining momentum across custom silicon design. Menta will be present to address this turning point: the growing demand for adaptable, long-lifecycle architectures in industrial and mission-critical systems. Menta will engage with system architects and semiconductor leaders on how embedded FPGA integration is moving beyond niche use cases and becoming a practical foundation for production-grade ASIC and SoC platforms.

Sophia-Antipolis, France | February 24, 2026 – At Embedded World 2026, Menta will join the global semiconductor and embedded systems community to address a structural shift redefining custom silicon architecture: the transition from static silicon platforms to adaptable, long-lifecycle systems.

Across industrial, mobility, aerospace, defense, edge AI, and security-critical markets, silicon is expected to remain qualified and reliable for decades. Yet the systems it supports continue to evolve — in algorithms, standards, connectivity, and security requirements.

This growing tension is reshaping architecture decisions. Embedded hardware reconfigurability is moving from optional capability to foundational design discipline.

At Embedded World 2026, Menta will present its standard-cell embedded FPGA (eFPGA) IP, enabling hardware reprogrammability to be integrated directly into production-grade ASIC and SoC platforms using industrial-grade design flows — without proprietary macros and without foundry constraints.

As heterogeneous architectures become mainstream, the ability to manage post-silicon evolution in a controlled and governable manner is becoming a defining competitive factor.

Menta’s approach focuses on:

- Integrating embedded FPGA IP within qualified ASIC and SoC flows
- Preserving long-term reliability and lifecycle governance
- Enabling heterogeneous acceleration within industrial constraints
- Supporting adaptable silicon platforms across critical markets

“Silicon used to be optimized for performance at tape-out,” said Vincent Markus, CEO of Menta. “Today, it must also be optimized for controlled evolution over decades. Embedded reconfigurability is no longer a feature — it is infrastructure.”

Menta’s team will welcome media, analysts, and industry partners at Hall 4 – Booth 4-459 throughout the event.

Press & Analyst Invitation

Menta’s executive and technical leadership will be available for interviews during and after Embedded World 2026 to discuss:

- The architectural shift toward adaptable silicon platforms
- Managing long-lifecycle ASIC and SoC evolution
- Embedded FPGA integration in industrial-grade semiconductor design
- The future of heterogeneous embedded systems

About Menta

Menta is a leading semiconductor IP and platform company with over 15 years of experience in programmable and adaptable silicon architectures. Originally recognized for its pioneering work in embedded FPGA (eFPGA) technology, Menta today addresses broader system-level challenges in advanced and chiplet-based designs, enabling semiconductor companies to build scalable, evolvable, and future-proof platforms.

For press inquiries or to schedule a meeting:

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