

## Press Release

### MENTA'S eFPGA TECHNOLOGY ADOPTED BY AIST FOR CRYPTOGRAPHY AND HARDWARE SECURITY PROGRAMS

*Security is becoming a central design parameter in semiconductor architectures, driving the shift toward adaptive and reconfigurable silicon.*

**Sophia Antipolis, France - April 22, 2026** - Menta, a pioneer in standard-cell-based embedded FPGA (eFPGA) IP, today announced that its eFPGA IP has been adopted by Japan's National Institute of Advanced Industrial Science and Technology (AIST) for programs in cryptography and hardware security.

Across the semiconductor industry, security, resilience, and supply chain trust are becoming central design parameters, driving increased focus on secure and lifecycle-controlled hardware architectures.

As security requirements evolve, semiconductor systems must increasingly be designed to adapt over time. This is particularly critical in cryptography, where algorithms must continuously evolve to address new standards, emerging threats, and future challenges such as quantum computing.

Extending the flexibility of traditional FPGA architectures into ASIC and SoC environments, Menta's standard-cell-based eFPGA IP enables native integration into advanced design flows, full compatibility with leading foundry ecosystems, and post-silicon reconfigurability without architectural compromise. These capabilities support the development of hardware architectures designed to evolve over time, reinforcing long-term system integrity and adaptability at the silicon level.

Japan remains a key environment for advanced semiconductor research and development, with a strong focus on industrial-grade reliability, long lifecycle systems, and trusted infrastructure. Menta continues to expand its footprint in Japan, including prior engagements such as Secafy.

*"Security is no longer a feature — it is becoming the organizing principle of modern semiconductor design,"* said Vincent Markus, President & CEO of Menta. *"Systems must now be designed to adapt to threats that do not yet exist."*

*This requires a new class of silicon — one that remains programmable, trusted, and performant throughout its lifecycle. That is precisely what Menta enables.”*

More broadly, the semiconductor industry is undergoing a transition toward adaptive, software-defined hardware architectures. In this context, eFPGA is emerging as a key building block for secure and flexible SoC designs.

This milestone reinforces Menta’s position as a trusted partner in security-critical ecosystems and highlights its continued momentum across global markets.

#### **About Menta SAS**

Menta is a semiconductor IP company focused on enabling adaptability at the silicon level. Its standard-cell embedded FPGA (eFPGA) technology allows ASIC and SoC designs to evolve over time while preserving the reliability, qualification, and long-term stability required in industrial-grade systems. Menta operates across global ecosystems, supporting the development of secure, adaptable, and long-lifecycle semiconductor platforms in industries where trust and resilience are critical.

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